

I'm not robot!

DESCRIPTION

The UTC PCR406 silicon controlled rectifiers are high performance planar diffused PNP devices. These parts are intended for low cost high volume applications.



TO-92

1:CATHODE 2:GATE 3:ANODE

ABSOLUTE MAXIMUM RATINGS

PARAMETERS	SYMBOL	TEST CONDITION	RATING	UNITS
Reverse Peak Off-State Voltage	V _{RRM}	T _j =40 to 125°C (I _{RM} =100)	400	V
			380	
On-State Current	I _{T(RMS)}	T _j =40°C	0.5	A
Average On-State Current	I _{T(AV)}	Half Cycle=50, T _{on} =40°C	0.5	A
Peak Reverse Gate Voltage	V _{GRM}	I _{CR} =10µA	1	V
Peak Gate Current	I _{GM}	10µs Max.	0.1	A
Gate Dissipation	P _{G(AV)}	20ms Max.	150	mW
Operating Temperature	T _j		-40-125	°C
Storage Temperature	T _{STG}		-40-125	°C
Soldering Temperature	T _s	1.6mm from case 150 Max.	260	°C

ELECTRICAL CHARACTERISTICS (T_{amb}=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	MAX	UNIT
Off state leakage current	I _{GR}	V _{GR} =10V, I _C =10µA, T _j =25°C		0.1	µA
Off state leakage current	I _{GR}	V _{GR} =10V, I _C =10µA, T _j =25°C		1.0	µA
On state voltage	V _T	I _T =1.4A		1.4	V
		I _T =1.5A		2.2	
On state threshold voltage	V _{T(TH)}	T _j =25°C		0.95	V
On state slope resistance	R _s	T _j =25°C		0.00	Ω
Gate trigger current	I _{GT}	V _D =7V		200	µA
Gate trigger voltage	V _{GT}	V _D =7V		0.9	V
Holding current	I _H	V _{GR} =10V		5	mA
Latching current	I _L	V _{GR} =10V		5	mA
Control rate of voltage rise	dv/dt	V _D =0.8V, I _{GR} =10µA, T _j =25°C			V/µs

UTC UNISONIC TECHNOLOGIES CO. LTD 1
09-K01-011A



2SC1383L/2SC1384L
NPN Silicon
General Purpose Transistor

RoHS Compliant Product
A suffix of "-C" signifies halogen & lead-free

TO-92L

FEATURE

Power dissipation

$P_{Diss} = 1 \text{ W (Tamb=25°C)}$

Collector current

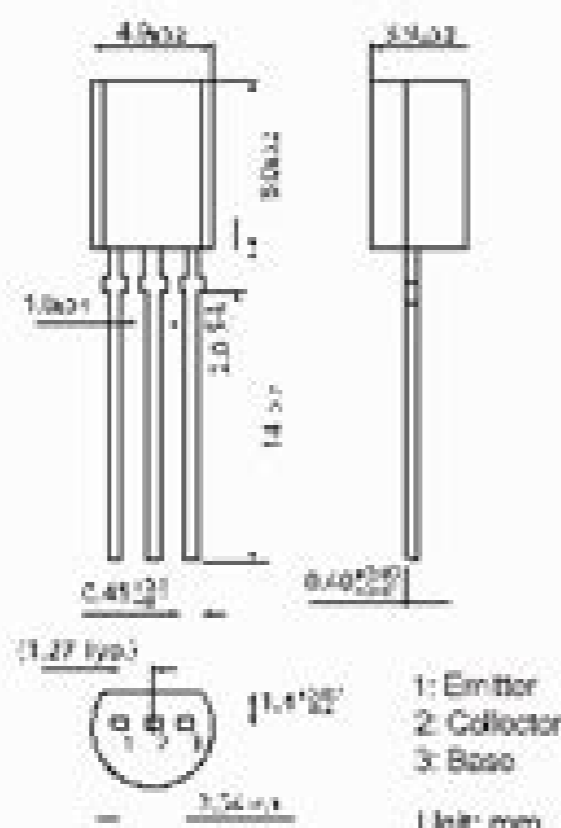
$I_{Cmax} = 1 \text{ A}$

Collector-base voltage

$V_{(BR)CBO} = 2SC1383L: 30 \text{ V}$
 $2SC1384L: 60 \text{ V}$

Operating and storage junction temperature range

$T_j, T_{stg}: -55°C \text{ to } +150°C$



ELECTRICAL CHARACTERISTICS (T_{amb}=25°C unless otherwise specified)

Parameter	Symbol	Test conditions	MIN	MAX	UNIT
Collector-base breakdown voltage	2SC1383L 2SC1384L V _{(BR)CBO}	I _C =10µA, I _E =0	30 60		V
Collector-emitter breakdown voltage	2SC1383L 2SC1384L V _{(BR)CEO}	I _C =2mA, I _B =0	25 50		V
Emitter-base breakdown voltage	V _{(BR)EB0}	I _C =10µA, I _E =0	5		V
Collector cut-off current	I _{CBO}	V _{CB} =20V, I _E =0		0.1	µA
DC current gain	h _{FE(1)}	V _{CE} =10V, I _C =500mA	85	340	
	h _{FE(2)}	V _{CE} =5V, I _C =1A	50		
Collector-emitter saturation voltage	V _{CE(sat)}	I _C =500mA, I _B =50mA		0.4	V
Base-emitter saturation voltage	V _{BE(sat)}	I _C =500mA, I _B =50mA		1.2	V
Transition frequency	f _T	V _{CE} =10V, I _C =50mA	100		MHz

CLASSIFICATION OF h_{FE(1)}

Rank	Q	R	S
Range	85-170	120-240	170-340

BC635, BC637, BC639, BC639-16

High Current Transistors

NPN Silicon

Features

- Pb-Free Package is Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEC}	45 60 80	Vdc
Collector-Base Voltage	V_{CBQ}	45 60 80	Vdc
Emitter-Base Voltage	V_{EBQ}	5.0	Vdc
Collector Current - Continuous	I_C	1.0	Adc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Device above 25°C	P_D	625 9.0	mW mW/°C
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Device above 25°C	P_D	800 12	mW mW/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-55 to +100	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

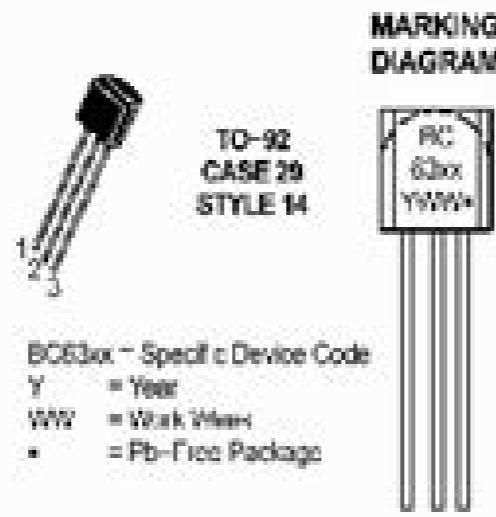
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	83.3	°C/W

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SDC D39964M1.



ON Semiconductor®

http://onsemi.com



ORDERING INFORMATION

Device	Package	Shipping ¹
BC635RL1	TO-92	2000/Tape & Reel
BC635711	TO-92	2000/Ammo Pack
BC637	TO-92	5000 Units/Box
BC639	TO-92	5000 Units/Box
BC635RL1	TO-92	2000/Tape & Reel
BC635ZL1	TO-92	2000/Ammo Pack
BC639-16Z11	TO-92	2000/Ammo Pack
BC639-16Z11G	TO-92 (Pb-Free)	2000/Ammo Pack

¹For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011D.

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May, 2004 - Rev. 4

1

Publication Order Number:
BC635D



MD2009DFX

High voltage NPN Power transistor for standard definition CRT display

General features

- State-of-the-art technology:
 - diffused collector "enhanced generation"
- More stable performance versus operating temperature variation
- Low base drive requirement
- Tighter h_{FE} range at operating collector current
- Fully insulated power package U.L. compliant
- Integrated free wheeling diode
- In compliance with the 2002/93/EC European directive

Applications

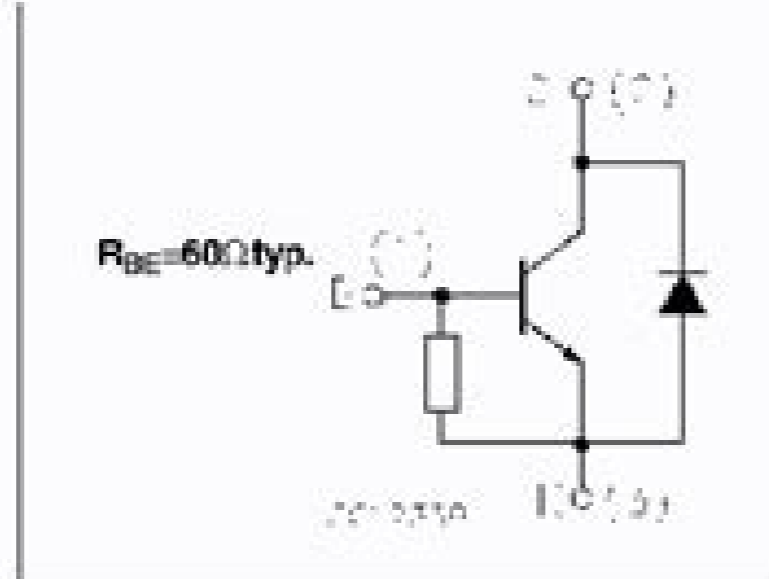
- Horizontal deflection output for TV

Description

The MD2009DFX is manufactured using Diffused Collector in Planar Technology adopting new and enhanced high voltage structure. The new MD product series show improved silicon efficiency bringing updated performance to the Horizontal Deflection stage.



Internal schematic diagram



Order codes

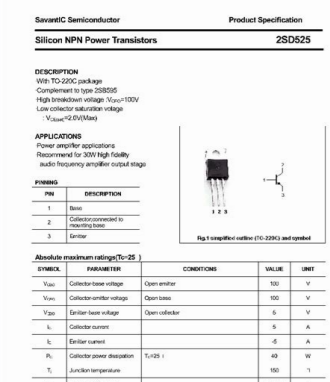
Part number	Marking	Package	Packaging
MD2009DFX	MD2009DFX	ISOWATT218FX	Tube

May 2006

Rev 3

1/10

www.st.com



S9013 Brief Description of S8050 S805050 is a NPN transistor the collector and issuer When the base pin is kept on the ground and it will be closed (in distorted forward) a signal is provided to the base pin. It has a maximum gain value of 400; This value determines the ability to amplify the transistor normally S8050. Because it is very high, it is normally used for amplification purposes. However, to a normal operating collector current, the typical value of the gain will be 110. The maximum amount of current that could flow through the collector's pin is 700 mA, so we can't drive loads that consume more than 700 mA using this transistor. To distinguish a transistor we must supply basic pin current, this current (IB) should be limited to 5Ma. When this transistor is completely distorted, it can allow a maximum of 700 mA to flow through the manifold and the emitter. This phase is called saturation region and the typical tension allowed through the otseuq otseuq ,rouf etnematepmoc atnevid rotsisnart li ,esab id etnerroc al assomir eneiV odnaU .V03 e V 02 etnemavittesir erness ebbertop)BCV(erottelloc id esab al o)ecéÁV(It is called as a cut-off region. S8050 in the push-Pull configuration as mentioned in the characteristics of the S8050 transistor is commonly used in the push pull configuration with the class B amplifier. So we discuss how it is done. A pushing amplifier, commonly known as class B amplifier is a type of multistage amplifier commonly used for the audio amplification of the speakers. It is very simple to build and requires two identical free transistors. For free it means that we need an NPN transistor and its equivalent PNP transistor. As here the NPN transistor will be S8050 and its equivalent PNP transistor will be S8550. Below is a simple circuit scheme of the class B amplifier with the use of the S8050. Circuit applications for audio amplification class B Amplifiers Transient Circuits Pulla Pulla where a low signal model is required 2D applications and dimensions if you are planning a PCB card or also with this component, then the following image from the transistor S8050 data sheet will be useful To know it type of package and size. Transistor S8050 data sheet you are reading a free preview page 2 is not shown in this preview. Full PDF Packagedownload Full PDF PackageThis Papera Short Summy of This Paper29 Full PDFS Related to this PaperdownLoadfdf Pack Part S8050 Description NPN Transistor Feature www.datasheet4u.com S8050 NPN General Purpose Transistors To-92 1. Emitter 2. COLLECTOR 1 2 2 3 Absolute Millets M aximum (TA = 25 C) Collection of O-Base-Base-based voltage raising Volta GE EMPROPER-BASE COLLECTOR Tension NT NT DISSIPATION Total dissipation ta = 25 C JUN CATION TEMPERATION TEMPERATION. VCO VCBE TEMBER SEA VCBO IC PD PD TJ TSTG VAL UE 25 40 5,0 500 0.625 150 -55 A +150 Unit Vdc VDC VDC MADC W C C Feature Electric Features TOR-EMITTER ROOM TUTTOM (IC = 0.1 MADC, IB = 0) Breakdown Collector-Base Breakdown (IC = 100 à e àpade, IE = 0) EMATORE-BAS AND E E Vol. Production Weitron Technology Datasheet Download S8050 Datasheet Part S8050 Description NPN Transistor Feature www.DataSheet4U.com S8050 NPN Shock transmitters Ppose Generali TO-92 1. EMITTER 2. BASE 3. COLLECTOR 1 3 RATINGS M AXIMUM (Ta=25 C) Rating Collector or-Emitter Collector-Base Volta ge Emitter-Base Voltage Retreat nt Total dissipation of the TA=25 C Jun ction Storage temperature, Simbolo temperature VCEO 0.6 VISTBO IC PD Tst O S8050 is um transistor de baixa tensão and high current, indicating para use em circuits amplificadores. Especificações: - Transistor S8050 (datasheet) - Tensão coletor-emissor Vceo: 20VDC - Tensão coletor-base Vcbo: 30VDC - Tensão emissor-base Vcbo: 5VDC - Current coletor Ic: 700mA - Encapsulation: TO-92 - Temperature de operação: -65 to 150°C 150°C



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